

DEC 05 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No.....09/512,149
Filing Date..... February 23, 2000
Inventor..... Vishnu K. Agarwal
Assignee..... Micron Technology, Inc.
Group Art Unit..... 2814
Examiner..... Marcos D. Pizarro-Crespo
Customer No.021567
Attorney Docket No..... MI22-1322
Title: Integrated Circuitry a Capacitor with a Amorphous and a Crystalline High
K Capacitor Dielectric Region

Art Unit 2814
Attention: Marcos D. Pizarro-Crespo
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR § 1.8

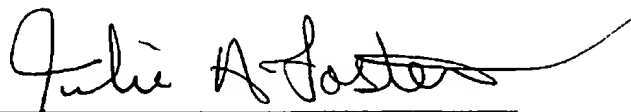
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Dated:

December 5, 2005

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/512,149
	Filing Date	February 23, 2000
	First Named Inventor	Vishnu K. Agarwal
	Art Unit	2814
	Examiner Name	Marcos D. Pizarro-Crespo
Total Number of Pages in This Submission	Attorney Docket Number	MI22-1322

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm Name	Wells St John, P.S.	
Signature	<i>Julie Foster for D. Brent Kenady</i>	
Printed name	D. Brent Kenady	
Date	December 5, 2005	Reg. No. 40,045

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Capacitor Dielectric Region

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

To: Assistant Commissioner for Patents
Washington, D.C. 20231

From: D. Brent Kenady
Tel. 509-624-4276; Fax 509-838-3424
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601 W. First Avenue, Suite 1300
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Sir:

Responsive to the Notice of Allowance dated September 19, 2005, Applicant
comments as follows:

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